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Title: Reduced Swing Charge Recycling Circuit
Arrangement And Adder Including The Same
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FIG. 1 PRIOR ART

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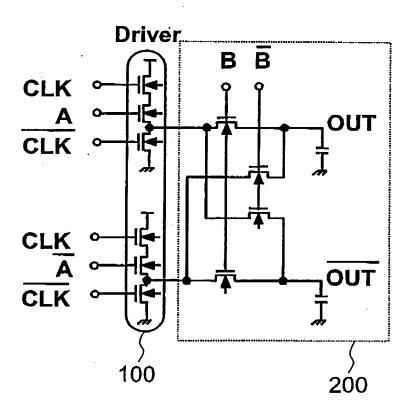
			· · · · · · · · · · · · · · · · · · ·	
	Final state	Drive	Initial state	
			# # # # # # # # # # # # # # # # # # #	Conventional
Precycle = Pconv/2			+Q/2 <u>+</u> Q/2 <u>+</u> <del>—</del> <del>—</del> <del>—</del> <del>—</del> <del>—</del> <del>—</del> <del>—</del> <del>—</del> <del>—</del> <del>—</del>	Charge recycling

FIG. 2 PRIOR ART

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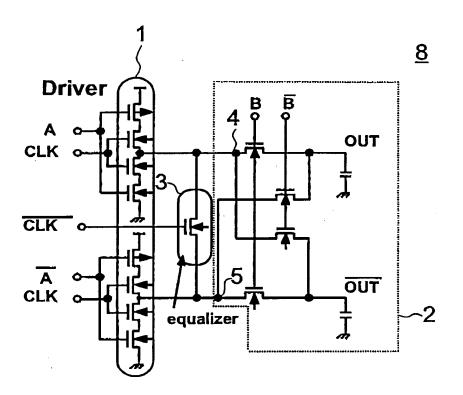
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FIG.3 PRIOR ART



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FIG.4



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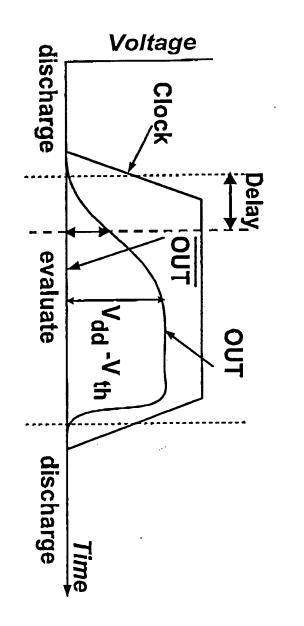


FIG. 5 PRIOR ART

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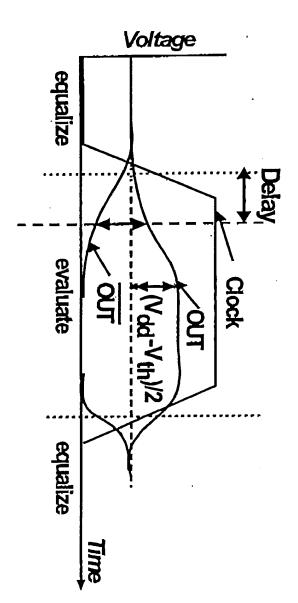
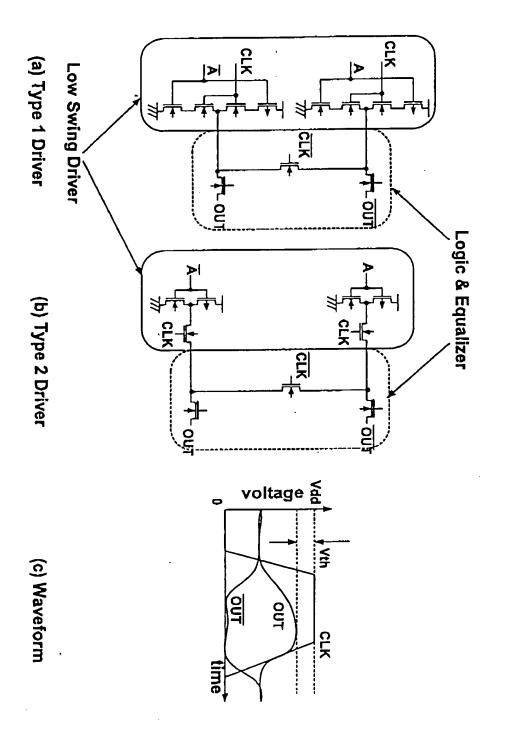


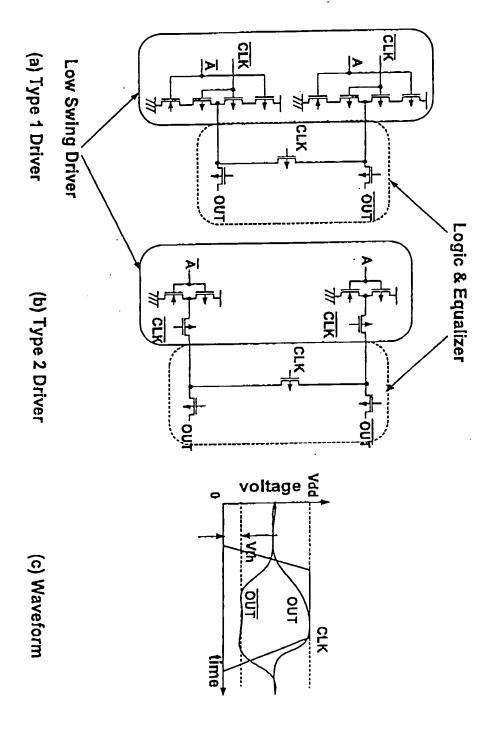
FIG. 6 PRIOR ART

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F1G. 8

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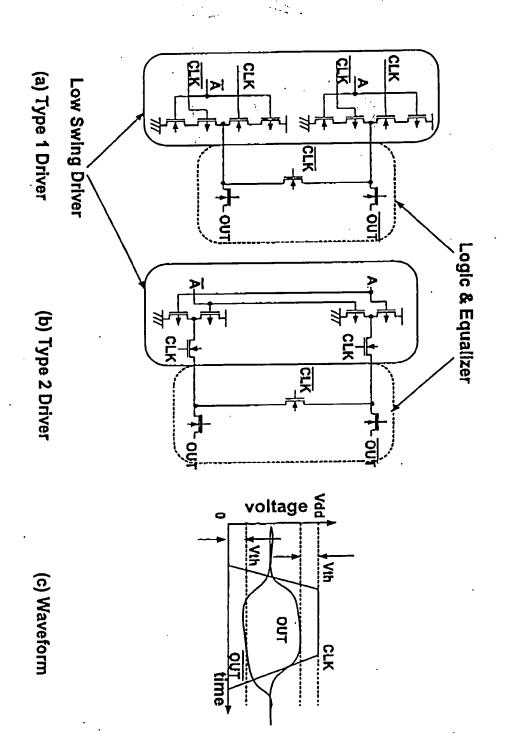
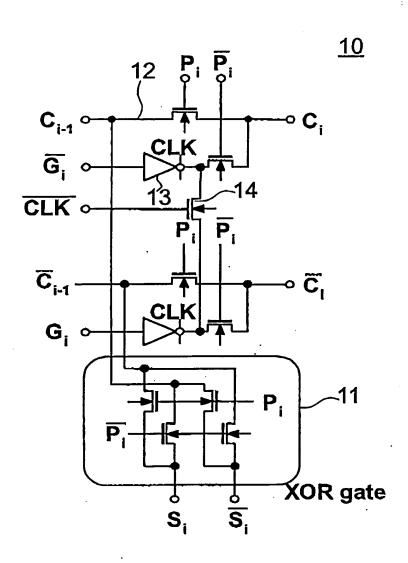


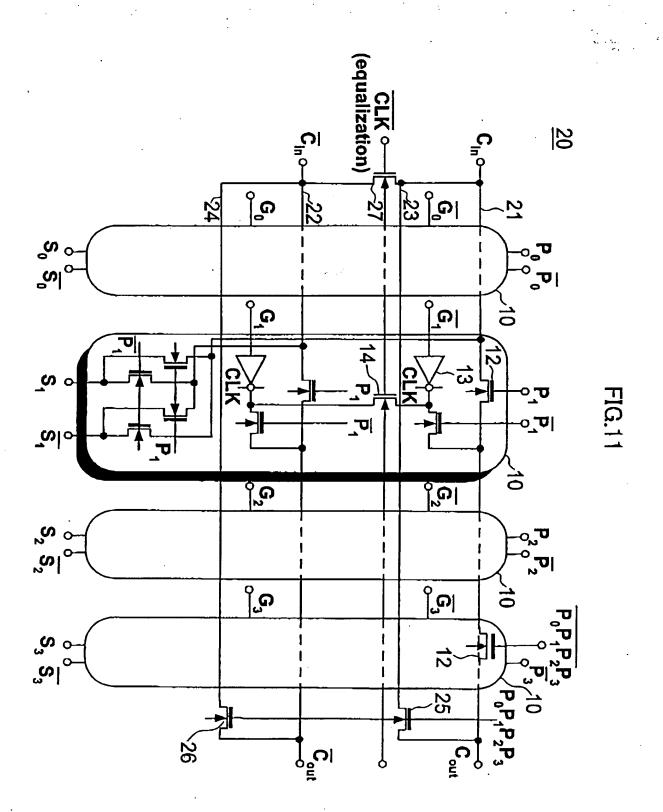
FIG. 9

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**FIG.10** 



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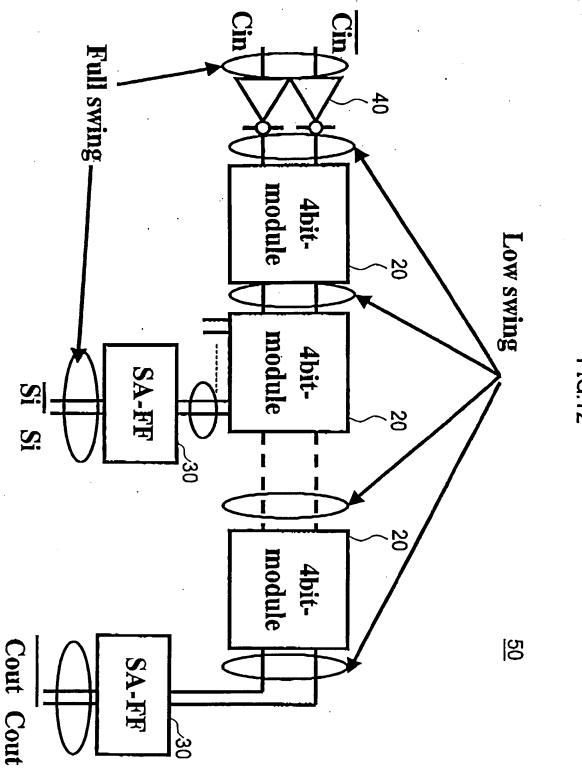


FIG.12

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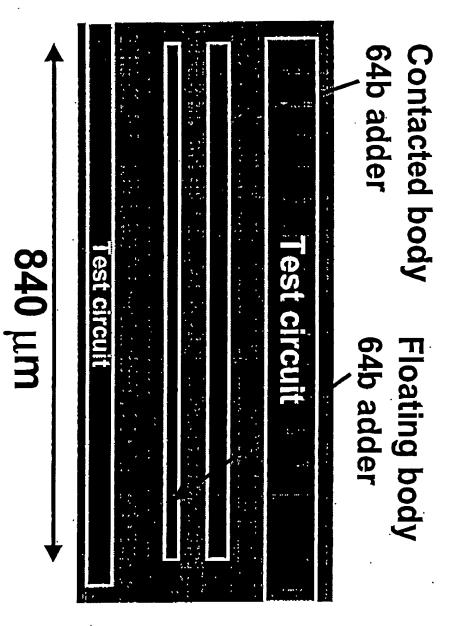
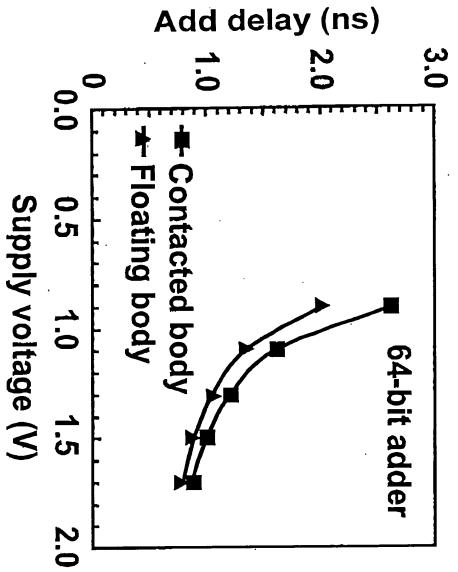


FIG. 13

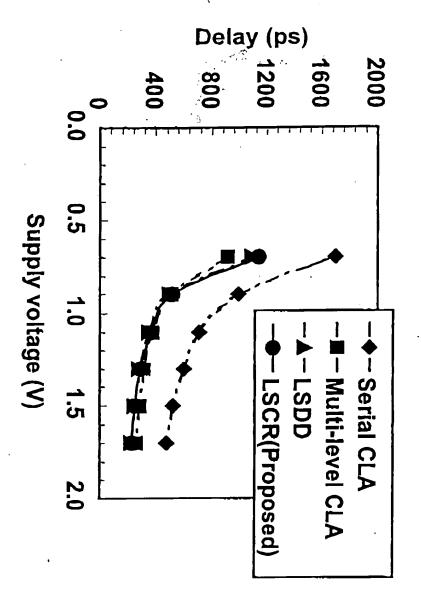
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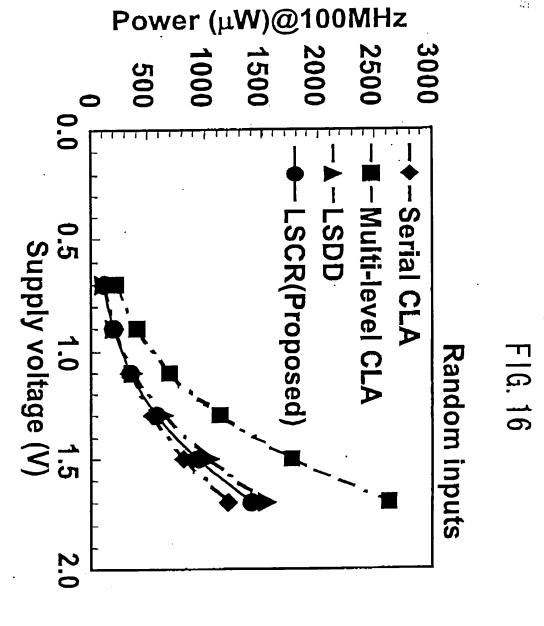
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